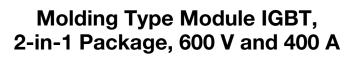
Vishay Semiconductors





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Double INT-A-PAK

PRODUCT SUMMARY					
V _{CES}	600 V				
I_C at T_C = 80 °C	400 A				
V _{CE(on)} (typical) at I _C = 400 A, 25 °C	1.60 V				
Speed	8 kHz to 30 kHz				
Package	Double INT-A-PAK				
Circuit	Half bridge				

FEATURES

- Low V_{CE(on)} trench IGBT technology
- Low switching losses
- 5 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 175 °C
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- UPS
- Switching mode power supplies
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultralow conduction loss as well as short circuit ruggedness. It is designed for applications such as UPS and SMPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		600	v
Gate to emitter voltage	V _{GES}		± 20	v
			530	
Collector current I _C		T _C = 80 °C	400	
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	800	А
Diode continuous forward current	I _F		400	
Diode maximum forward current	I _{FM}		800	
Maximum power dissipation	PD	T _J = 175 °C	1600	W
Short circuit withstand time	t _{SC}	T _J = 125 °C	5	μs
l ² t-value, diode	l ² t	$V_{R} = 0 V, t = 10 ms, T_{J} = 125 \ ^{\circ}C$	10 900	A ² s
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature.

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IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL TEST CONDITIONS MIN. TYP.		MAX.	UNITS		
Collector to emitter breakdown voltage	V _{(BR)CES}	V_{GE} = 0 V, I_C = 2 mA, T_J = 25 $^\circ C$	600	-	-	
Collector to emitter saturation voltage	V _{CE(on)}	V_{GE} = 15 V, I_C = 400 A, T_J = 25 $^\circ C$	-	1.6	2.05	v
Collector to enlitter saturation voltage		V_{GE} = 15 V, I_C = 400 A, T_J = 175 $^\circ C$	-	2.0	-	
Gate to emitter threshold voltage	V _{GE(th)}	V_{CE} = V_{GE} , I_C = 4 mA, T_J = 25 °C	4.0	-	6.5	
Zero gate voltage collector current	I _{CES}	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{T}_{\text{J}} = 25 ^{\circ}\text{C}$	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE}=V_{GES},V_{CE}=0~V,T_J=25~^\circ C$	-	-	400	nA

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	35	-	ns
Rise time	t _r		-	70	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 400 \text{ V}, \text{ I}_{C} = 400 \text{ A}, \text{ R}_{g} = 1.3 \Omega,$	-	180	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	75	-	
Turn-on switching loss	E _{on}		-	14.1	-	mJ
Turn-off switching loss	E _{off}		-	10.0	-	
Turn-on delay time	t _{d(on)}		-	37	-	ns
Rise time	tr		-	72	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 400 \text{ V}, \text{ I}_{C} = 400 \text{ A}, \text{ R}_{g} = 1.3 \Omega,$	-	220	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, \text{ T}_{J} = 175 \text{ °C}$	-	84	-	
Turn-on switching loss	E _{on}		-	23.2	-	
Turn-off switching loss	E _{off}		-	16.8	-	mJ
Input capacitance	C _{ies}		-	30.8	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 30 V, f = 1.0 MHz	-	2.12	-	nF
Reverse transfer capacitance	C _{res}		-	0.92	-	
SC data	I _{SC}	$\label{eq:tsc} \begin{split} t_{sc} &\leq 5 \; \mu s, \; V_{GE} = 15 \; V, \; T_J = 125 \; ^{\circ}C, \\ V_{CC} &= 360 \; V, \; V_{CEM} \leq 600 \; V \end{split}$	-	TBD	-	А
Internal gate resistance	R _{gint}		-	1.3	-	Ω
Stray inductance	L _{CE}		-	-	20	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

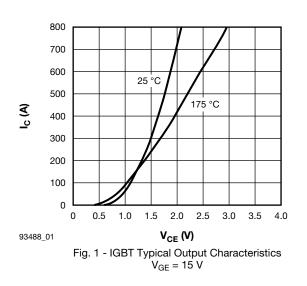
DIODE ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)									
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS		
Diode forward voltage	V _F	$V_{\rm F}$ $I_{\rm F} = 400 \rm{A}$	T _J = 25 °C	-	1.38	1.80	v		
Dide forward voltage			T _J = 125 °C	-	1.41	-			
Diode reverse recovery charge	Q _{rr}	Q _{rr}	T _J = 25 °C	-	15.5	-			
Didde reverse recovery charge			T _J = 125 °C	-	28.5	-	μC		
Diede peak reverse recevery eurrent	I _{rr}			$I_F = 400 \text{ A}, V_R = 300 \text{ V},$ dI/dt = -7000 A/µs,	T _J = 25 °C	-	265	-	А
Diode peak reverse recovery current		$V_{GF} = -15 V$	T _J = 125 °C	-	335	-	A		
Diede reverse recevent energy	E _{rec}		T _J = 25 °C	-	3.5	-	ml		
Diode reverse recovery energy			T _J = 125 °C	-	7.5	-	mJ		

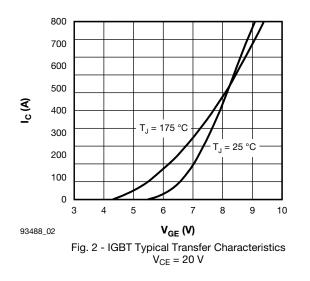
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THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	TJ		-	-	175	°C
Storage temperature range	T _{Stg}		-40	-	125	
Junction to case IGBT	P		-	-	0.094	
per ½ module Diode	R _{thJC}		-	-	0.158	K/W
Case to sink	R _{thCS}	Conductive grease applied	-	0.035	-	
Mounting torque		Power terminal screw: M6	:	2.5 to 5.0)	Nm
Mounting torque		Mounting screw: M6	3.0 to 5.0		INITI	
Weight				300		g





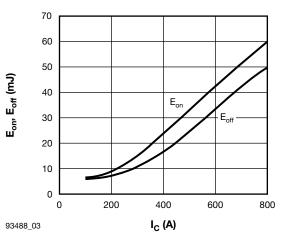
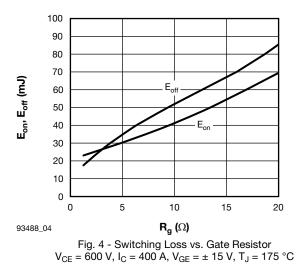


Fig. 3 - IGBT Switching Loss vs. Collector Current V_{CC} = 600 V, R_g = 1.3 Ω , V_{GE} = ± 15 V, T_J = 175 °C



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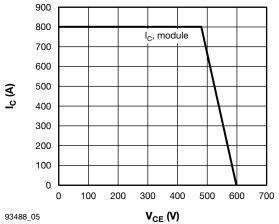
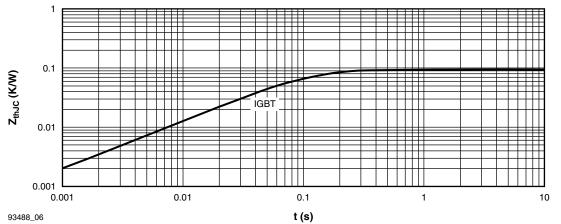
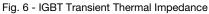
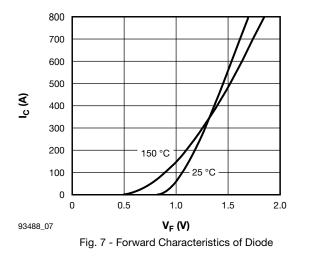


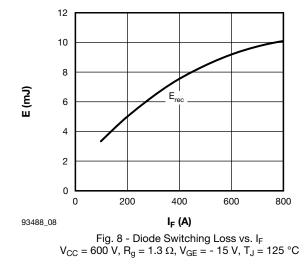
Fig. 5 - RBSOA R_g = 1.3 Ω , V_{GE} = ± 15 V, T_J = 175 °C







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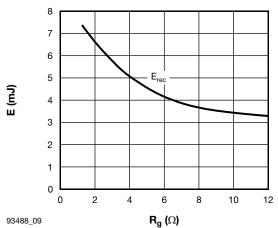


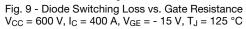
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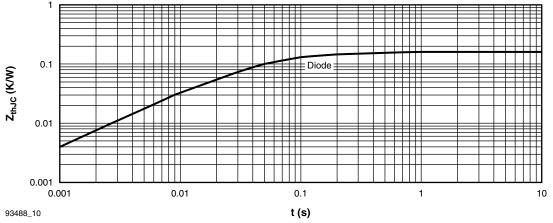
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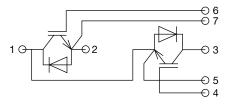






CIRCUIT CONFIGURATION

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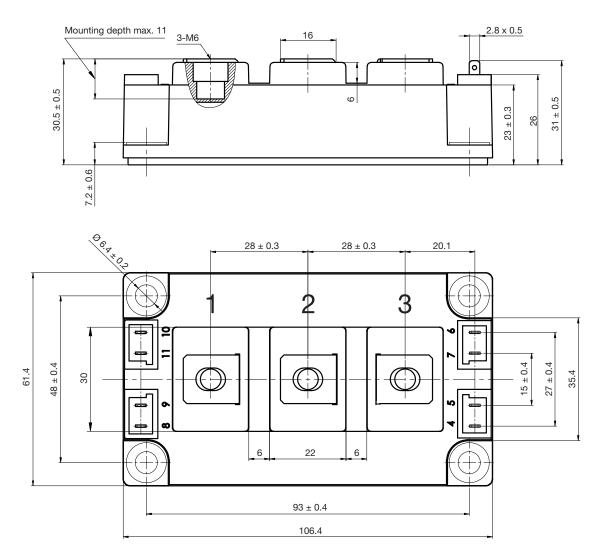
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95525		



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Double INT-A-PAK

DIMENSIONS in millimeters (inches)





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